5 4 3 2 1

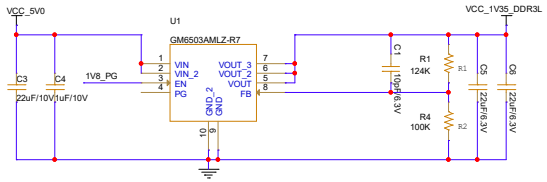
D C B A

PH1P100SBG484无时序要求

A7电源时序：

VIN_5V --> VCCINT_FPGA
--> PHYVCCA_FPGA
--> PHYVCCT_FPGA
--> VCCAUX_1V8_FPGA --> VCC_1V35_DDR3L
--> VCC_3V3

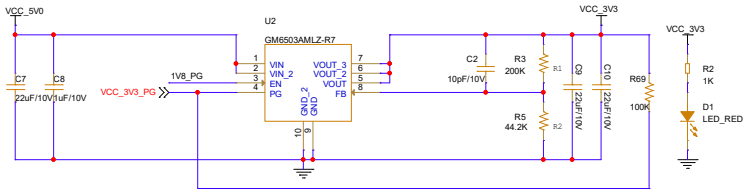
1V35 3A



输入电压：2.5V-5.5V
输出电压：0.6V-VIN
输出电流：3A
工作温度范围：-40~125℃
VREF:0.6V±0.1%
VEN>1.1V

Vout=0.6x(1+R1/R2)

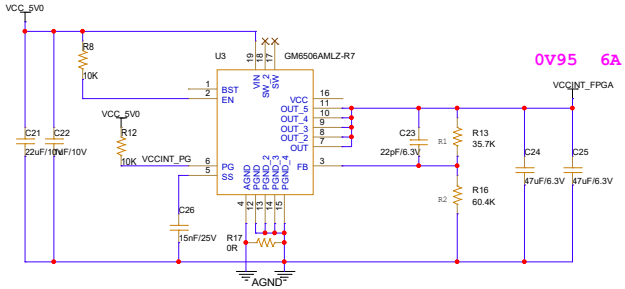
3V3 3A



输入电压：2.5V-5.5V
输出电压：0.6V-VIN
输出电流：3A
工作温度范围：-40~125℃
VREF:0.6V±0.1%
VEN>1.1V

Vout=0.6x(1+R1/R2)

0V95 6A



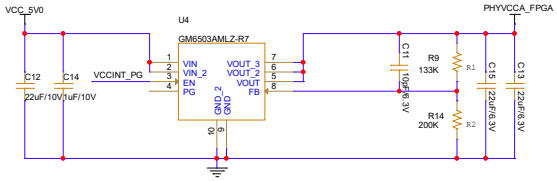
AGND和GND中间串个0R电阻

输入电压：2.5V-5.5V
输出电压：0.6V-VIN
输出电流：6A
工作温度范围：-40~125℃
VREF:0.6V±0.1%
VEN>1.1V

Vout=0.6x(1+R1/R2)

VOUT=0.6*(1+R1/R2)
Vout=0.95V, R1=35.7K R2=60.4K
Vout=1.0V, R1=10K R2=15K
PH1P50为0.95V, A7为1.0V

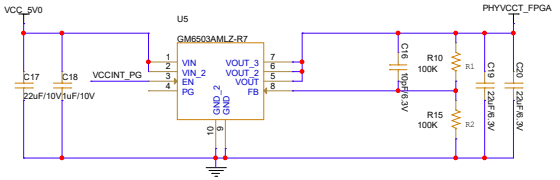
1.0V 3A



输入电压：2.5V-5.5V
输出电压：0.6V-VIN
输出电流：3A
工作温度范围：-40~125℃
VREF:0.6V±0.1%
VEN>1.1V

Vout=0.6x(1+R1/R2)

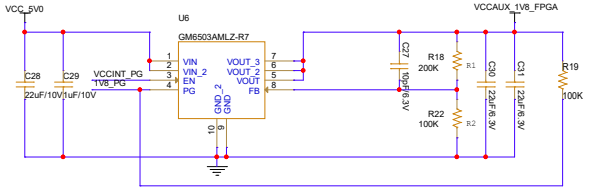
1.2V 3A



输入电压：2.5V-5.5V
输出电压：0.6V-VIN
输出电流：3A
工作温度范围：-40~125℃
VREF:0.6V±0.1%
VEN>1.1V

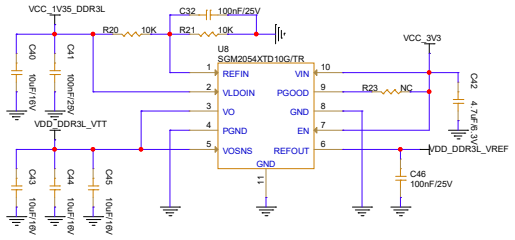
Vout=0.6x(1+R1/R2)

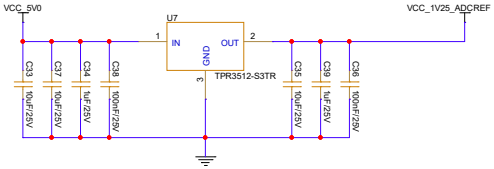
1V8 3A



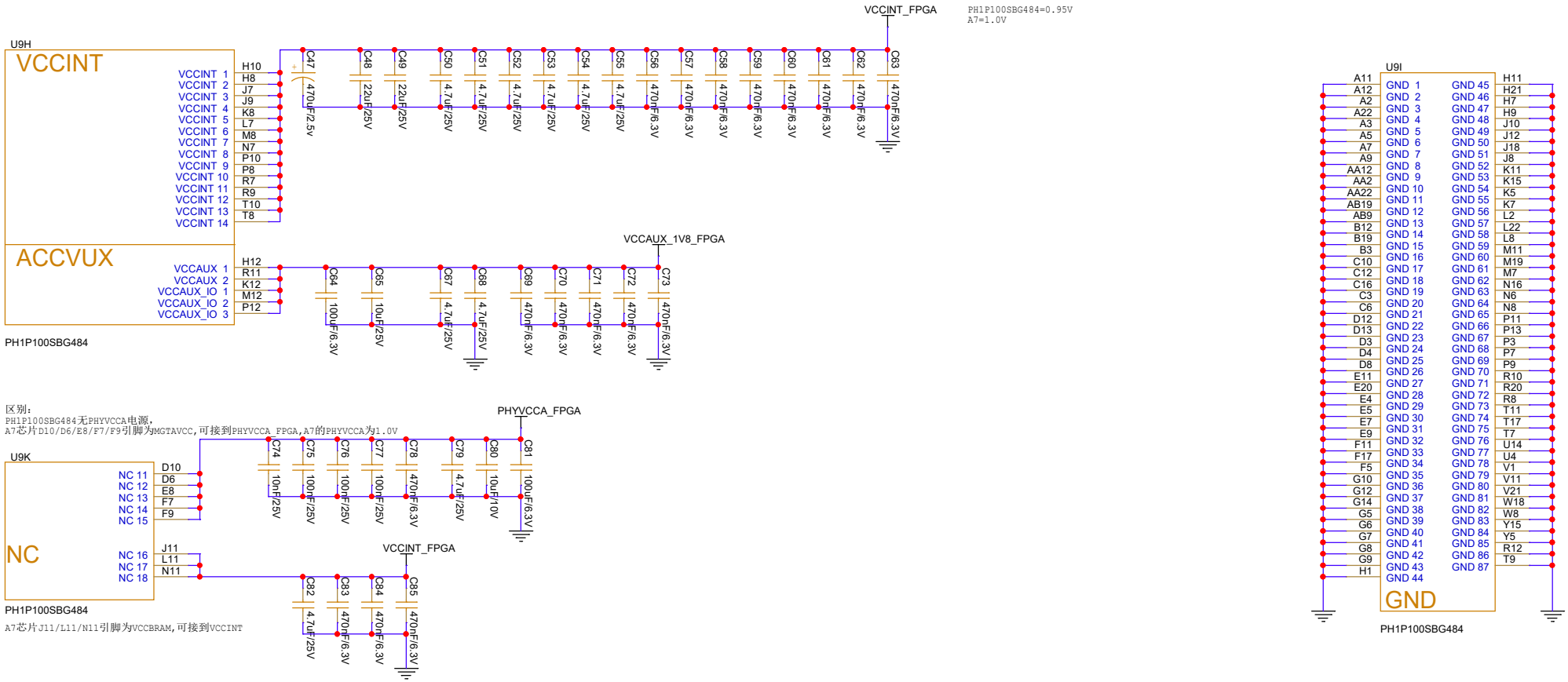
输入电压：2.5V-5.5V
输出电压：0.6V-VIN
输出电流：3A
工作温度范围：-40~125℃
VREF:0.6V±0.1%
VEN>1.1V

Vout=0.6x(1+R1/R2)



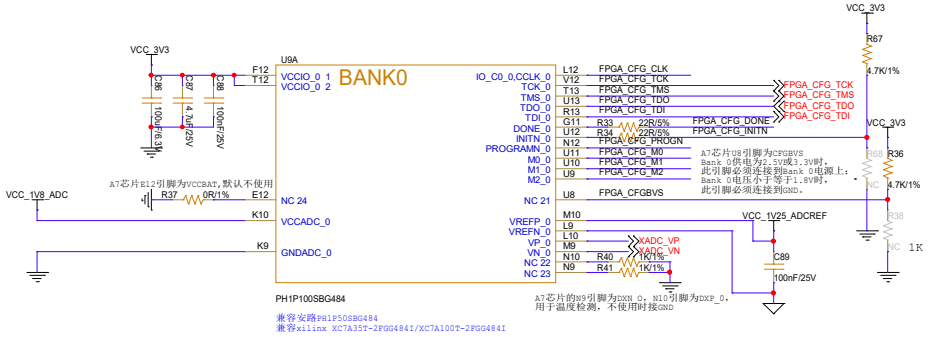


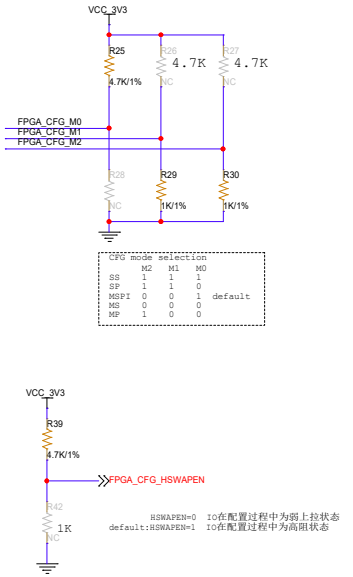
米联客 MILIANKE
www.uisrc.com
CA05
ProjectName: CA05
Size: A2 Page Title: P01_SYS_POWER Rev:
Date: Tuesday, July 14, 2026 Sheet 1 of 8



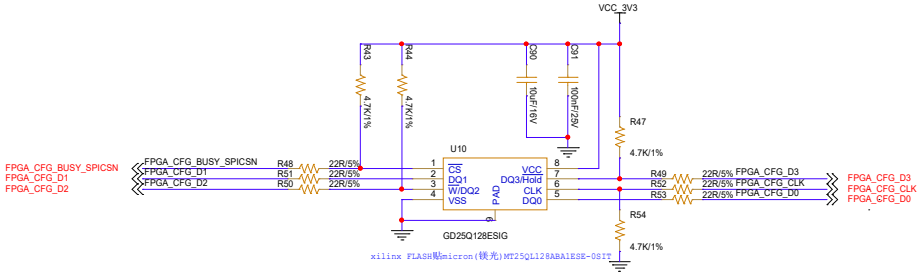
米联客	MILIANKE
www.uisrc.com	CA05
ProjectName: CA05	
Size: B	Page Title: P02_FPGA_POWER Rev:
Date: Tuesday, July 14, 2026	Sheet 2 of 8

BANK0

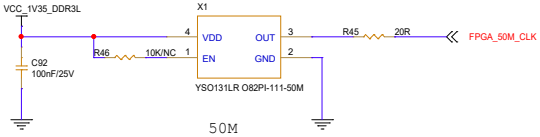




SPI_FLASH



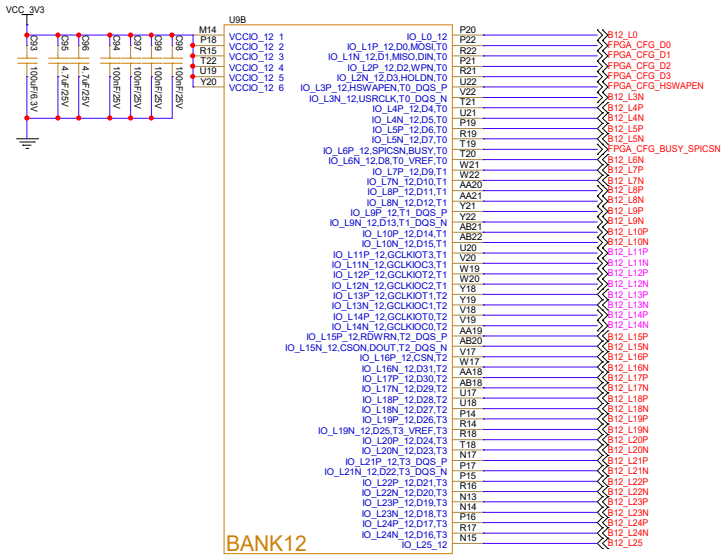
CLK_1V35_50M



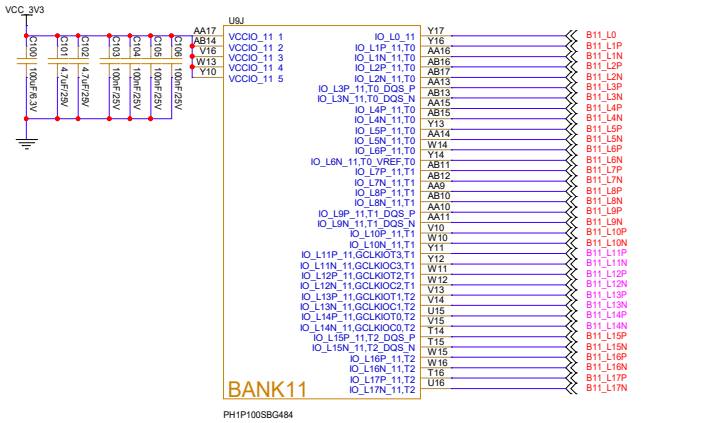
米联客 MILIANKE
www.uisrc.com CA05

ProjectName: CA05			
Size: C	Page Title: P03_FPGA_BANK0	Rev:	
Date: Thursday, May 14, 2026	Sheet 3 of 8		

A7芯片AA17/AB14/V16/W13/Y10引脚为VCCO_13



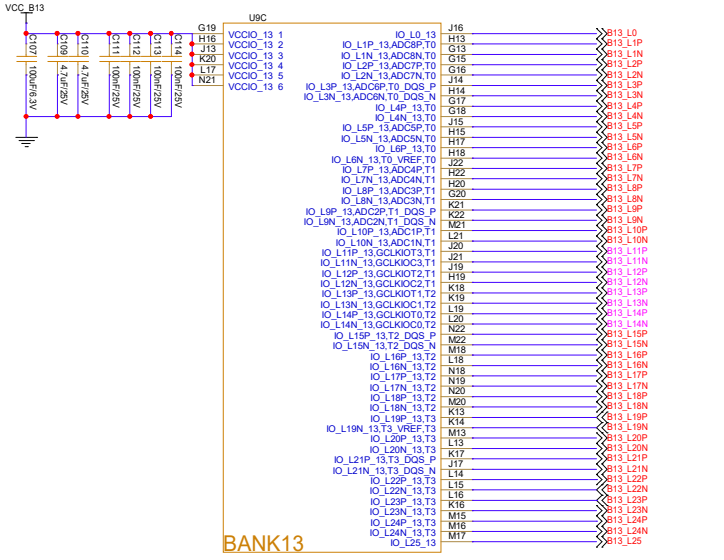
A7-BANK14



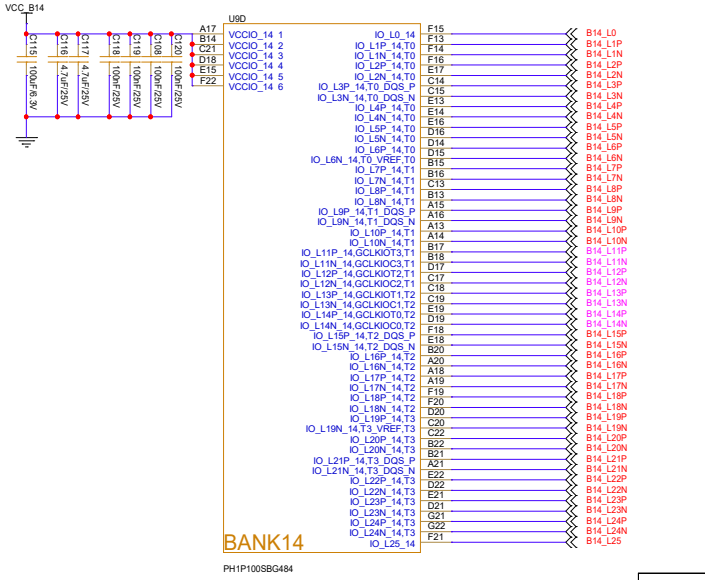
A7-BANK13

PH1P50SBG484/XC7A35T-2FGG484I无BANK11

A7-BANK15



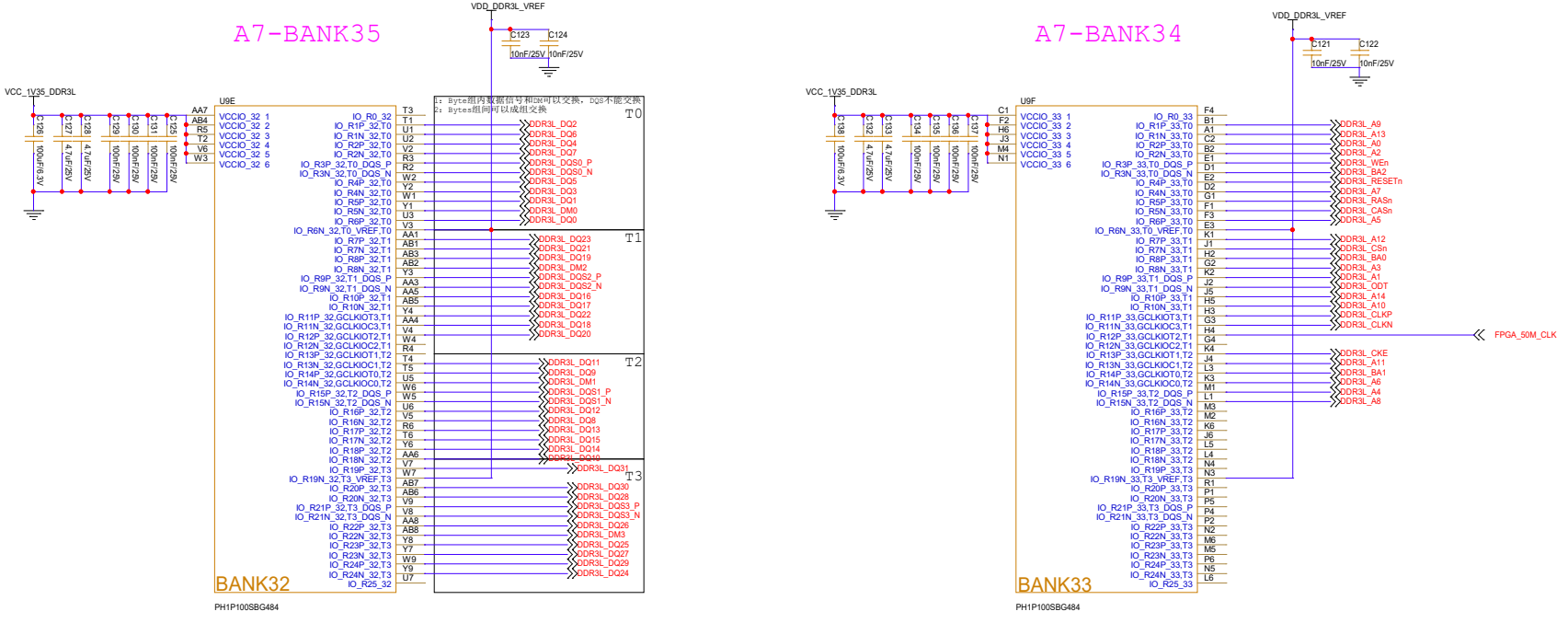
A7-BANK16



米联客 MILIANKE

www.uisrc.com CA05

ProjectName:	CA05		
Size: C	Page Title: P04_FPGA_BANK11_12_13	14Rev:	
Date: Thursday, May 14, 2026	Sheet 4 of 8		

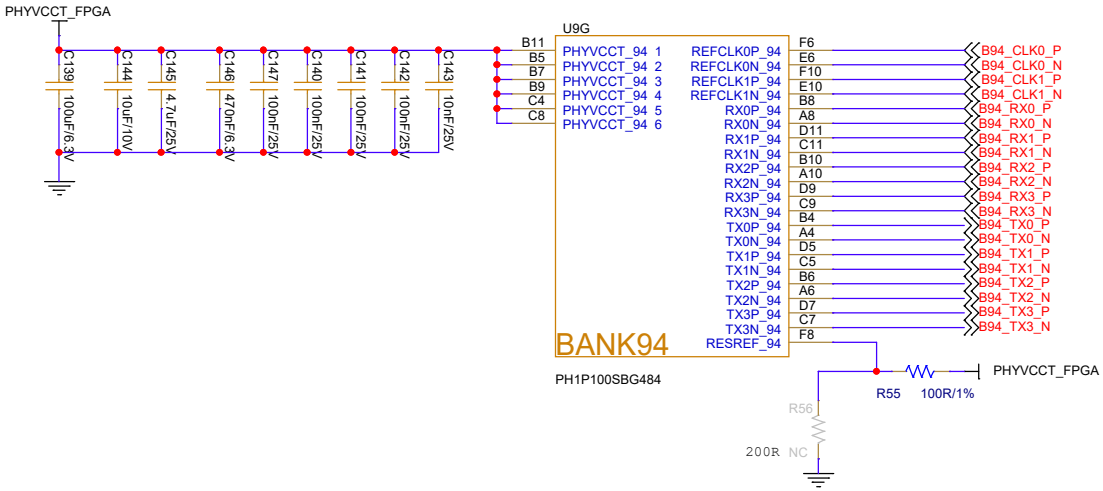


米联客 MILIANKE

www.uisrc.com CA05

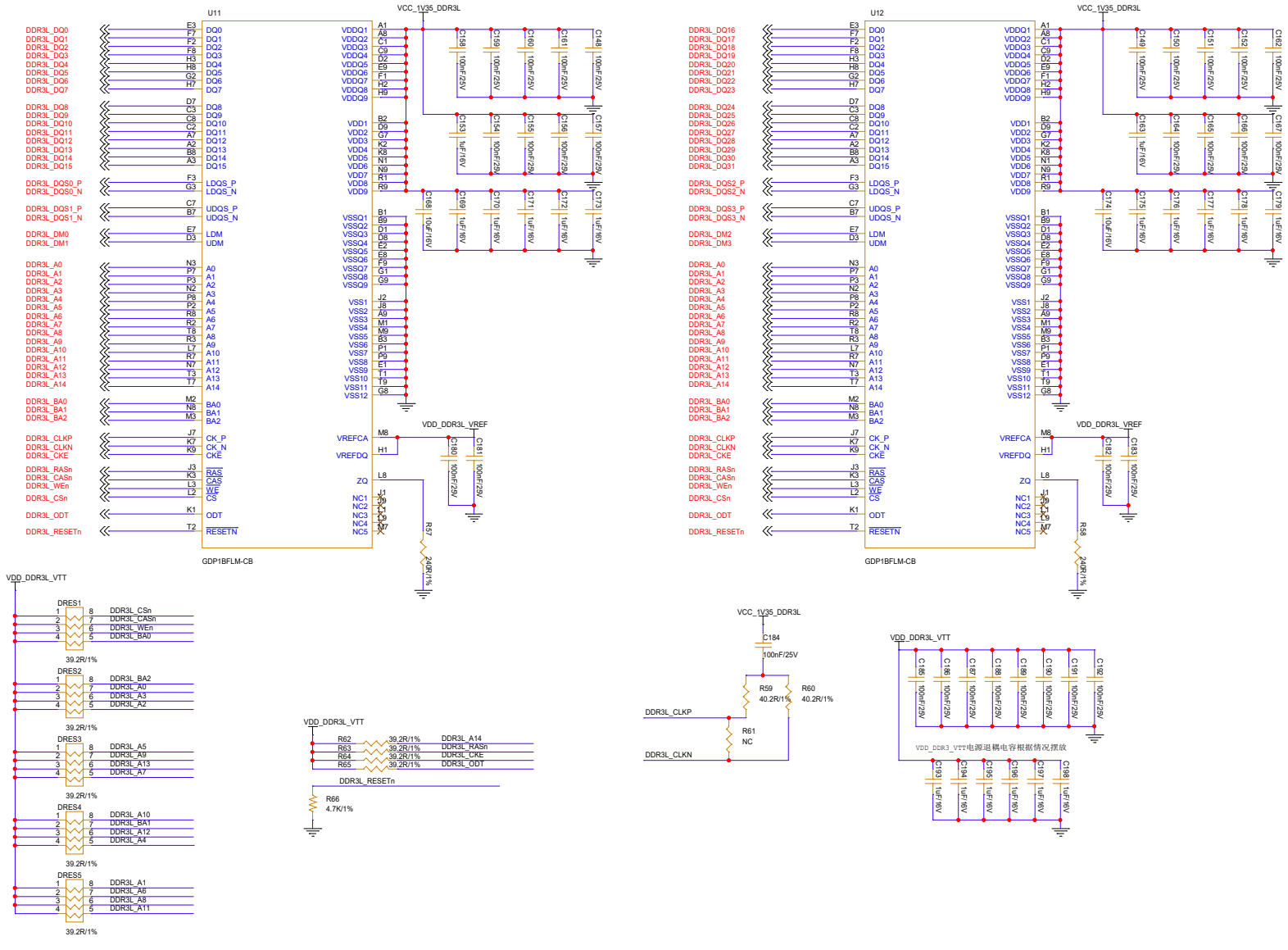
ProjectName:	CA05		
Size: C	Page Title: P05_FPGA_BANK32_33	Rev:	
Date: Thursday, May 14, 2026	Sheet 5 of 8		

A7-BANK216



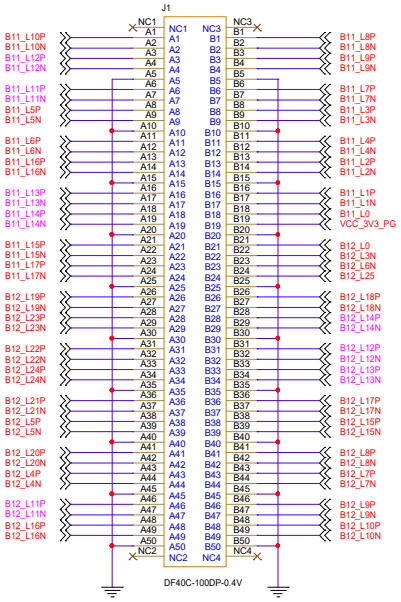
米联客 MILIANKE	
www.uisrc.com	CA05
ProjectName: CA05	
Size: B	Page Title: P06 FPGA Serdes BANK94 Rev:
Date: Thursday, May 14, 2026	Sheet 6 of 8

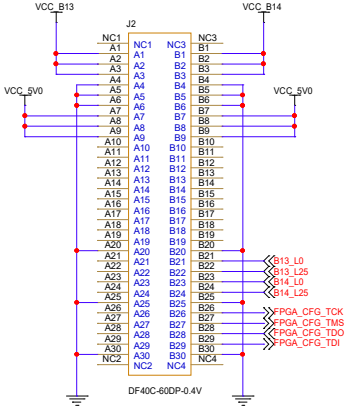
PH1P50SBG484/XC7A35T-2FGG484I不贴U12

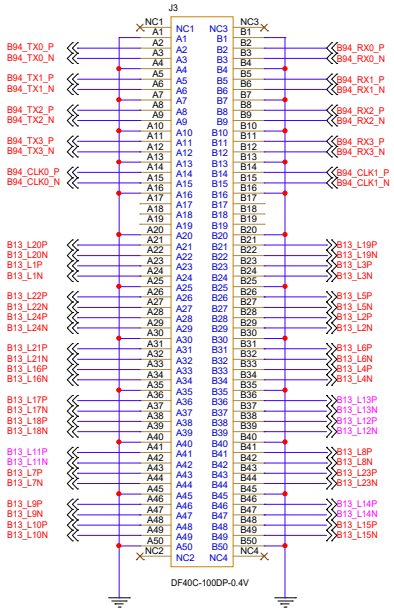


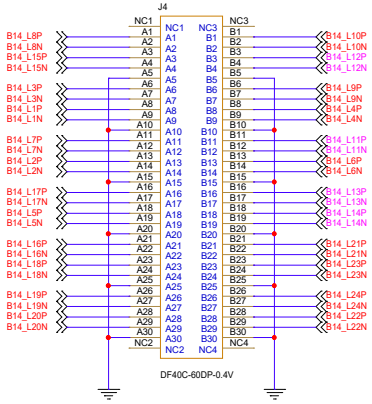
米联客 MILIANKE
www.uisrc.com CA05

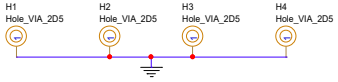
ProjectName:	CA05		
Size: C	Page Title: P07_DDR3L_X32	Rev:	
Date: Thursday, May 14, 2026	Sheet 7 of 8		











Hole 散热片固定孔

米联客	MILIANKE		
www.uisrc.com		CA05	
ProjectName:	CA05		
Size: C	Page Title: P08_CON	Rev:	
Date: Friday, March 13, 2026	Sheet 8 of 8		